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PATENT

Serial No. 10/538,628

Amendment in Reply to Office Action mailed on August 22, 2006

IN THE SPECIFICATION

Please amend the specification as follows:

Replace the paragraph on page 3, lines 16-22 of the specification with the following:

The right term of the right side of the expression (2) represents the new memory loop shown in FIG 2, while the complementing left term represents an alternative embodiment of a repetitive controller that can be used to deal with periodic signals that completely lack half-wave symmetry. When looking at the amplitude plot of the two terms (Fig. 4 right and left terms of the right side of the expression (2) (where Fig. 5 is a plot associated with the right term and Fig. 6 is a plot associated with the left term of the right side of the expression (2)), given in dB with respect to normalized frequencies expressed in radians, it is clear that the right term has infinite gain peaks at the odd harmonic frequencies, while the complement has infinite gain peaks at the even harmonic frequencies. Fig 4 shows an amplitude plot for the classical memory loop (with N = 32) shown in Fig. 1.